

REPLACEMENT SHEET

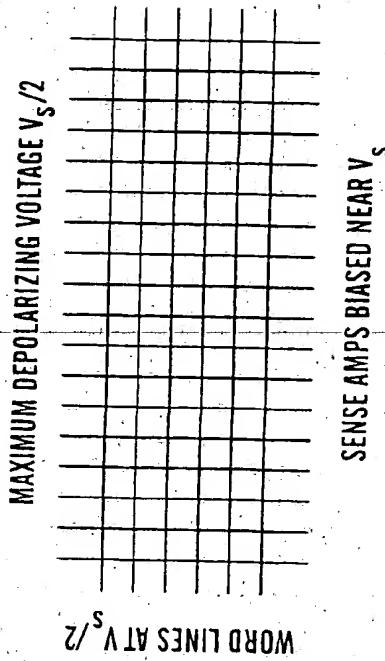
4/15

Accepted /OK

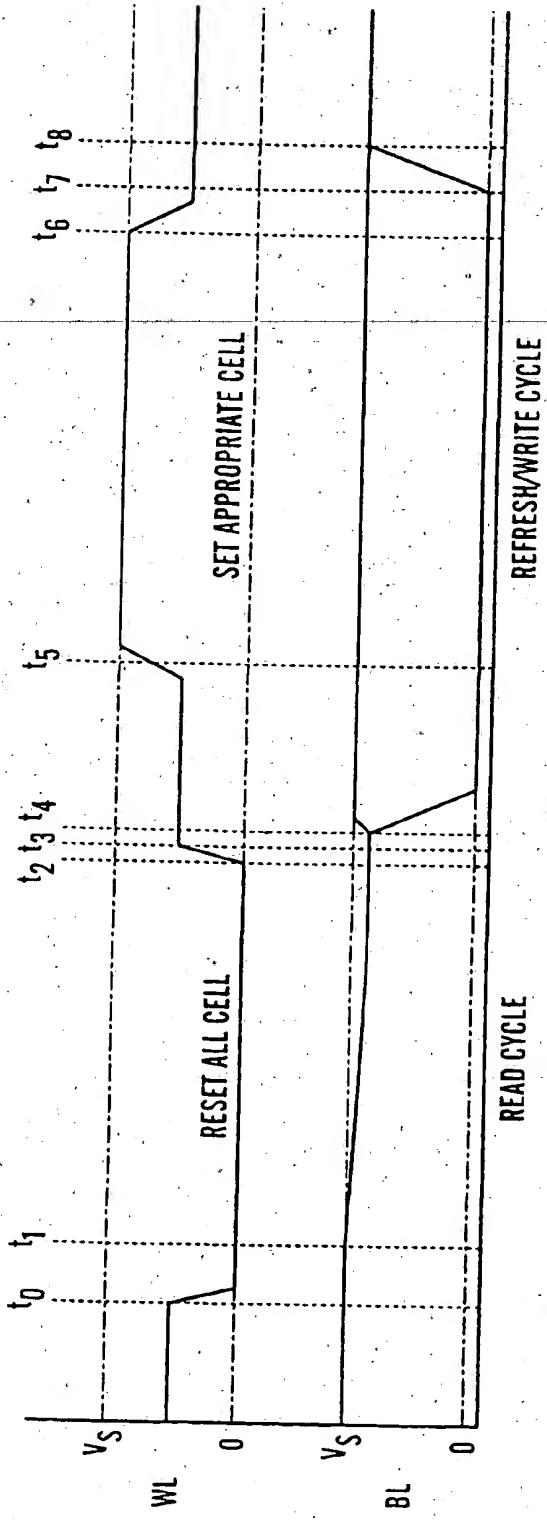
5/20/04

JMM

- t_0 : WORD LINE CLAMP RELEASED, ACTIVE PULLDOWN TO 0
- t_1 : BIT LINE CLAMP RELEASED - SENSE AMP ON
- t_2 : BIT LINE DECISION - DATA LATCHED
- t_3 : WORD LINE RETURNED TO QUIESCENT $V_s/2$
- t_4 : WRITE DATA LATCHED ON BIT LINES
- t_5 : WORD LINE PULLED TO V_s - SET/RESET CELL
- t_6 : WORD LINE RETURNED TO QUIESCENT $V_s/2$
- t_7 : BIT LINES ACTIVELY RETURNED TO V_s CLAMP
- t_8 : READ/WRITE CYCLE COMPLETE



SENSE AMPS BIASED NEAR V_s



REFRESH/WRITE CYCLE

Fig. 4

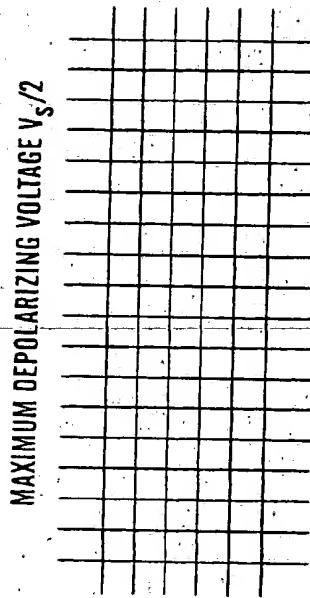


REPLACEMENT SHEET

5/15

Accepted
5/20/04
JMB

- t₀: WORD LINE LATCHED, ACTIVE PULL UP TO V_s
 - t₁: BIT LINE CLAMP RELEASED - SENSE AMP ON
 - t₂: BIT LINE DECISION - DATA LATCHED
 - t₃: WORD LINE RETURNED TO QUIESCENT $V_s/2$
 - t₄: WRITE DATA LATCHED ON BIT LINES
 - t₅: WORD LINE PULLED TO 0 - SET/RESET CELL
 - t₆: WORD LINE RETURNED TO QUIESCENT $V_s/2$
 - t₇: BIT LINES ACTIVELY RETURNED TO 0 CLAMP
 - t₈: READ/WRITE CYCLE COMPLETE



WORD LINES AT V₂^S

SENSE AMPS BIASED NEAR V_S

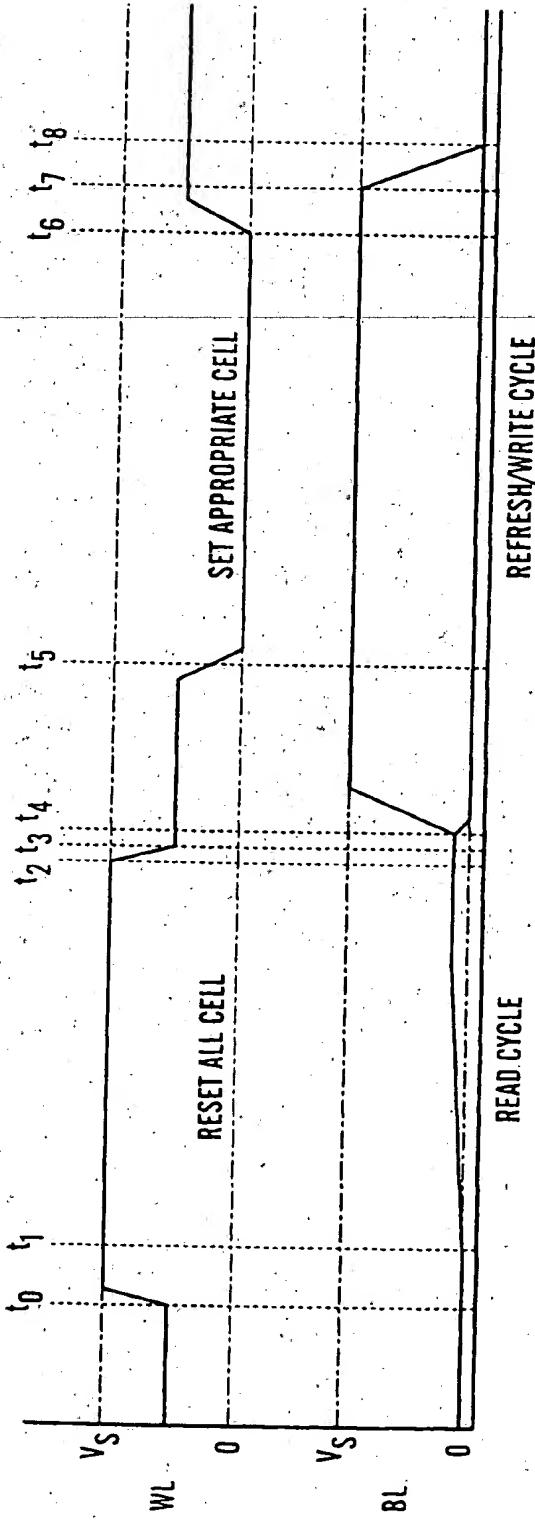


Fig. 5. *Leucosia* *lutea* (Fabricius).